

Figure 1

FIG. 2 is a block diagram of an ATM module 101, showing its internal components and external interfaces. The module includes a State RAM Data memory (112), AAL5 Module (105), AAL2 Module (102), Cell Buffer (104), ATM Processor (120), UTOPIA L2 (108), Cell DMA I/F (106), and a Chip/Peripheral Interconnect Module (140). External interfaces include VTDMA I/F, External I/F (145), and a VBUS for peripheral interconnect / PCI/XBUS for external.

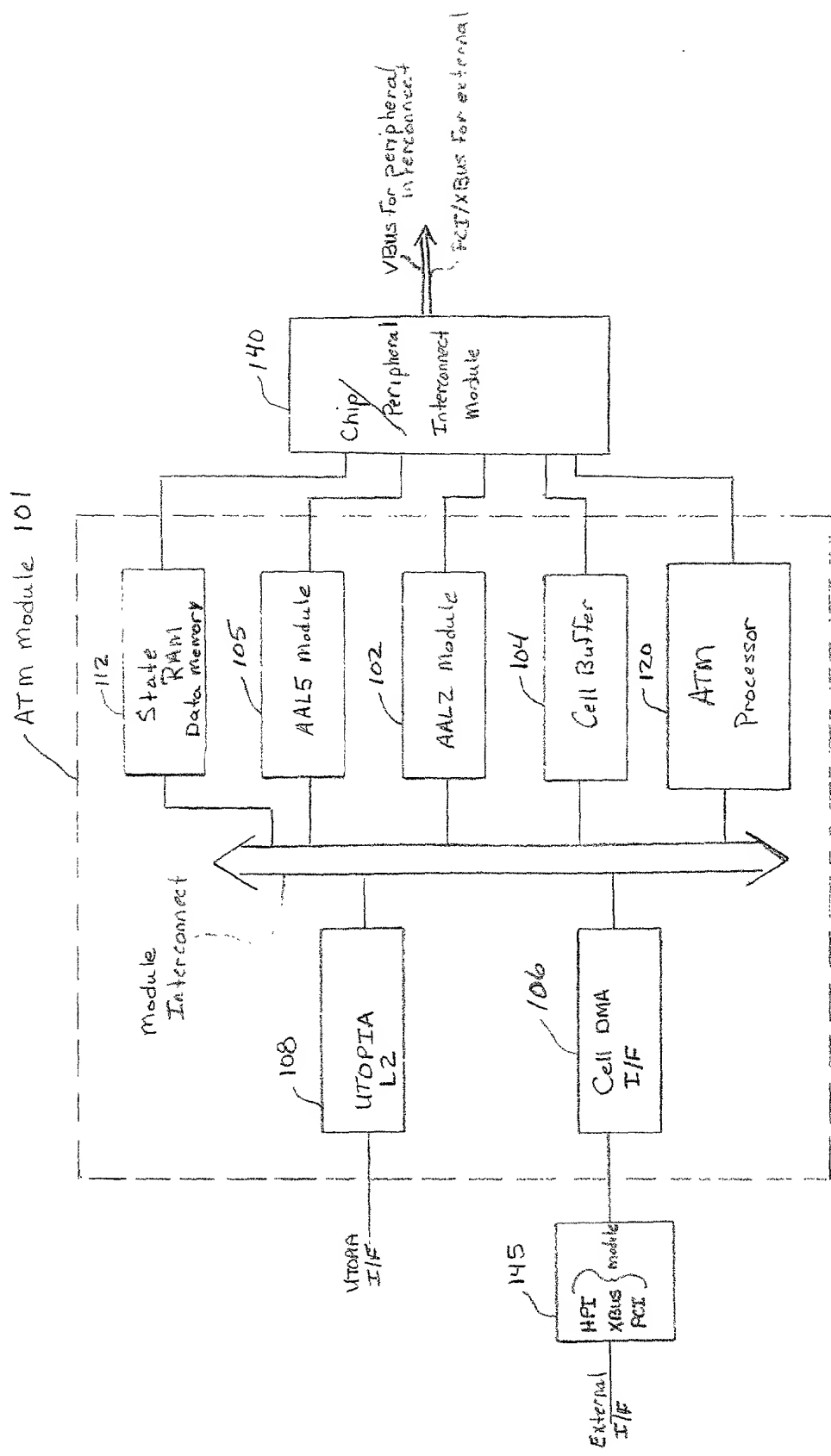


Figure 2

ATM Processor 120

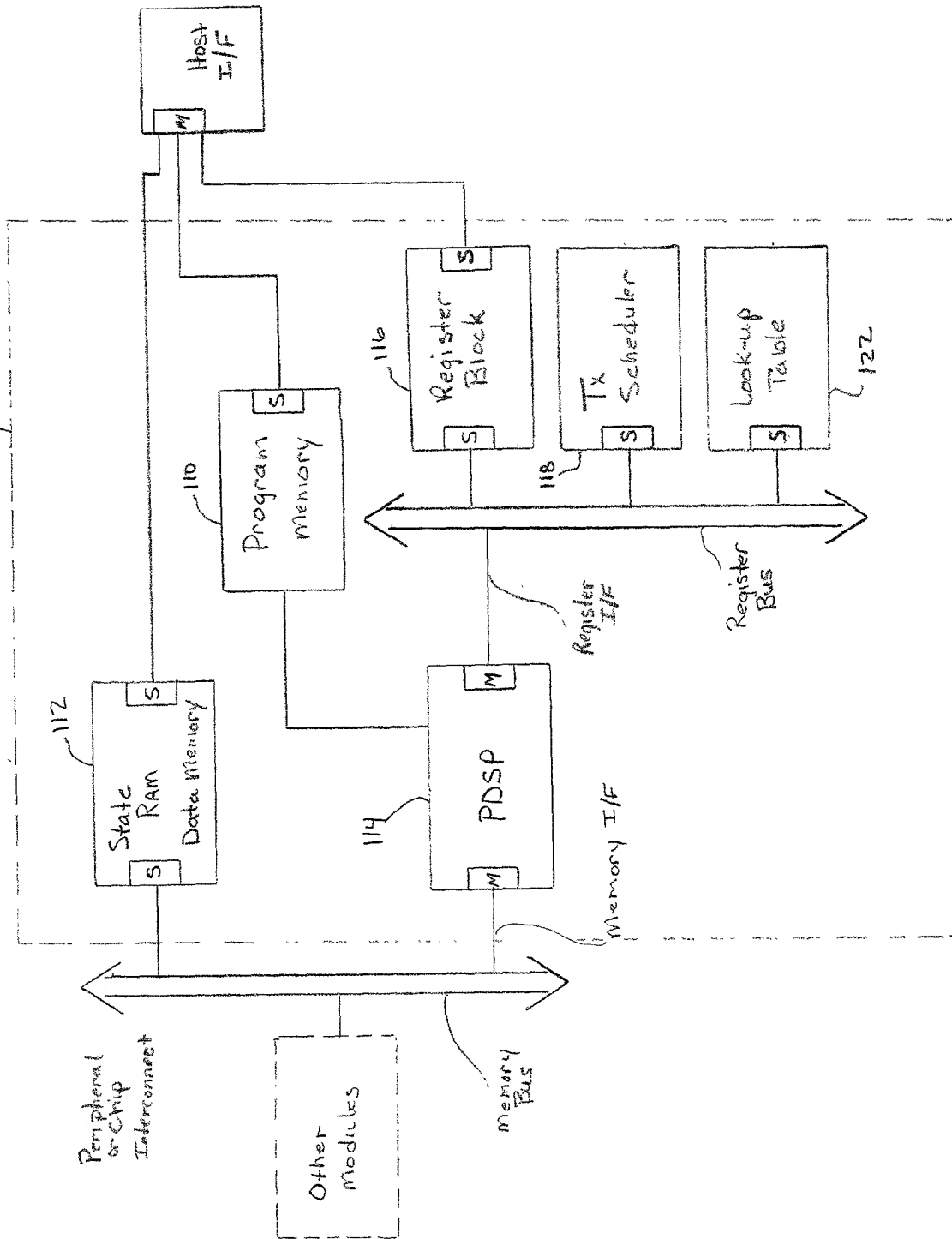


Figure 3

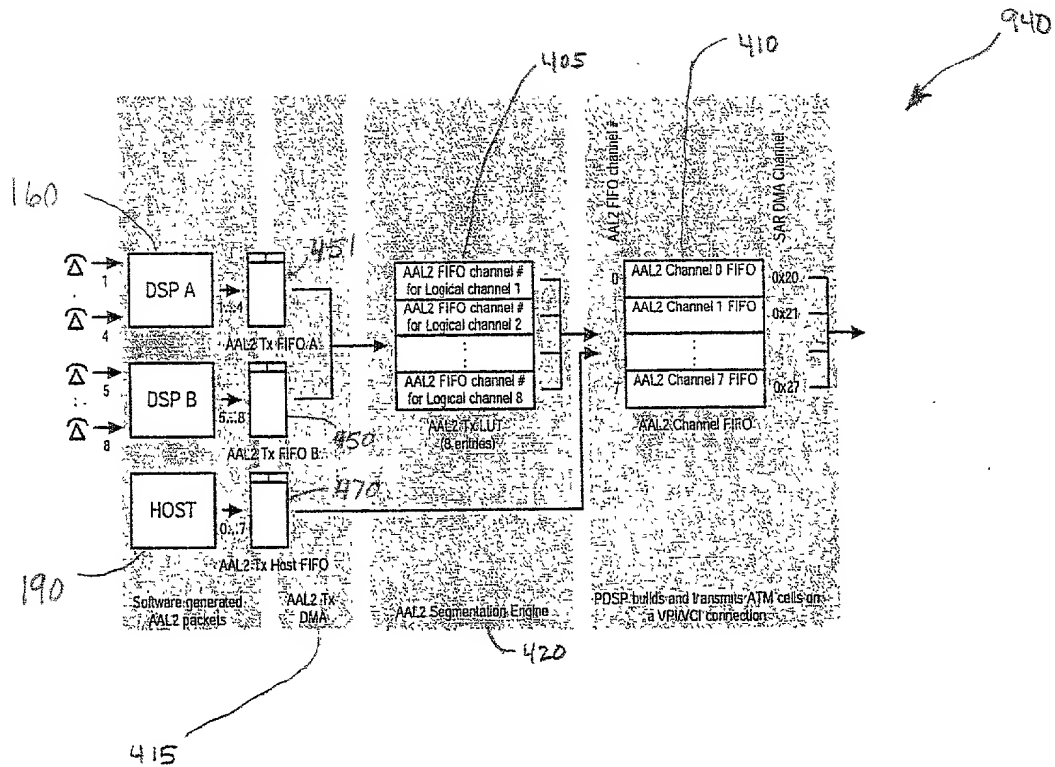


Figure 4

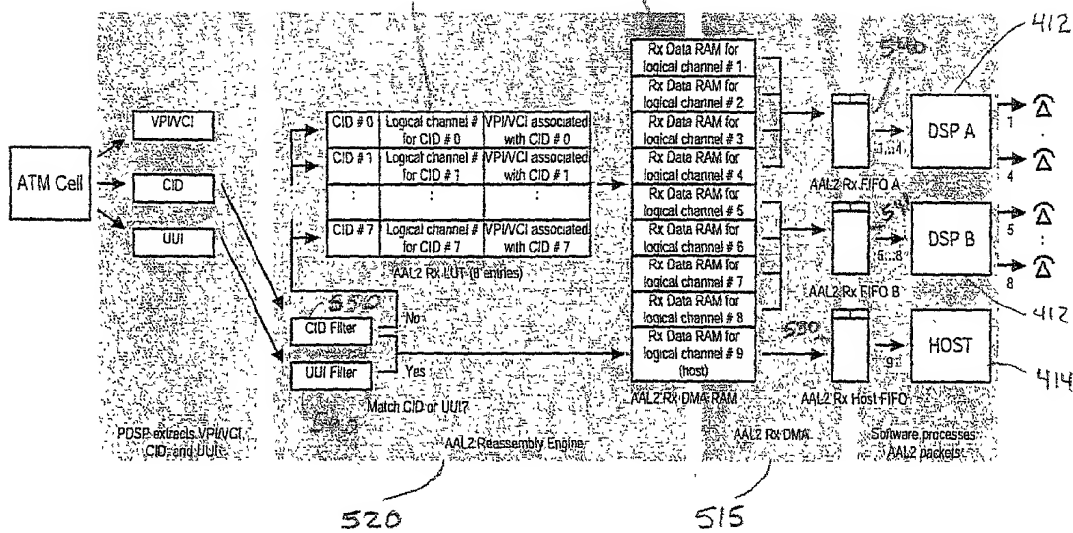


Figure 5

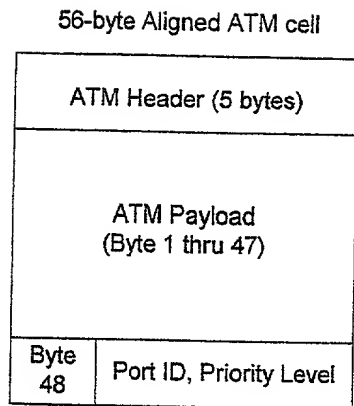


Figure 7

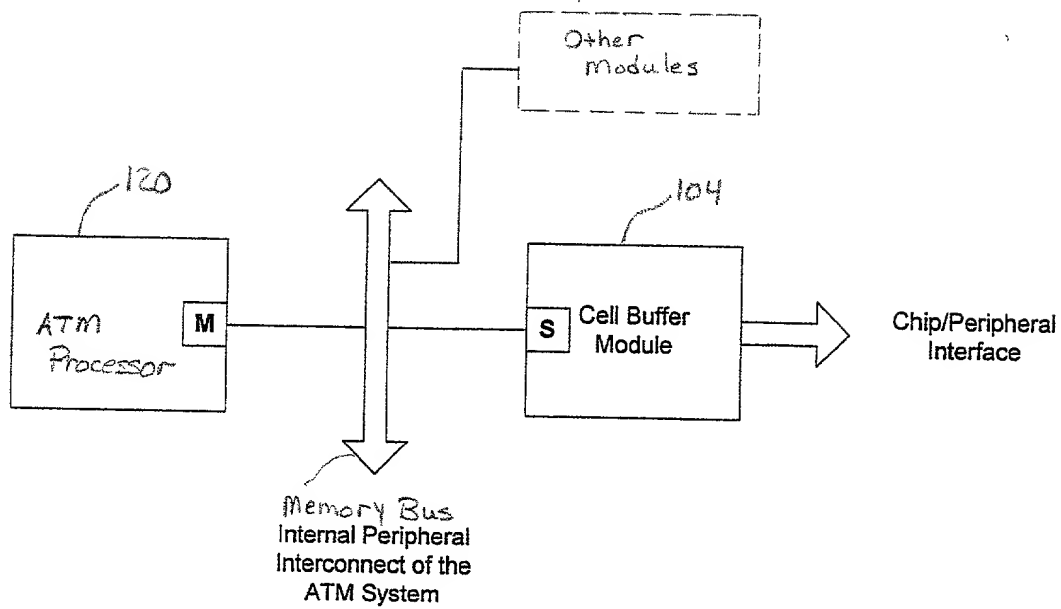


Figure 6A

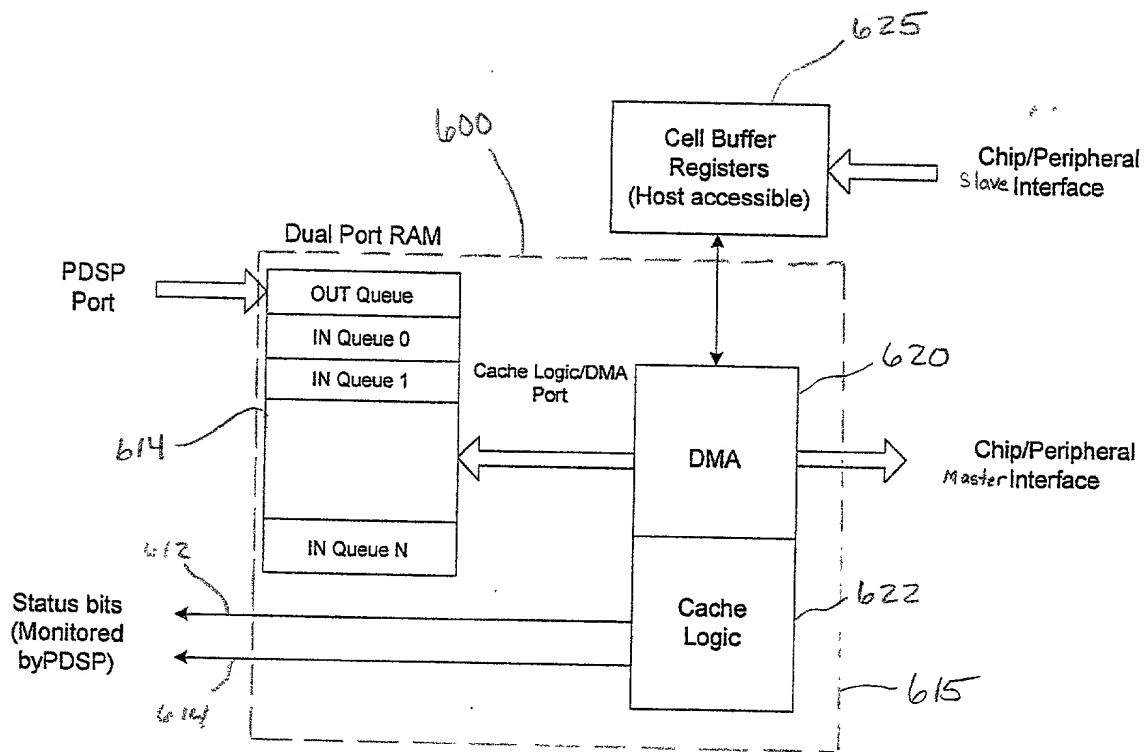


Figure 6B

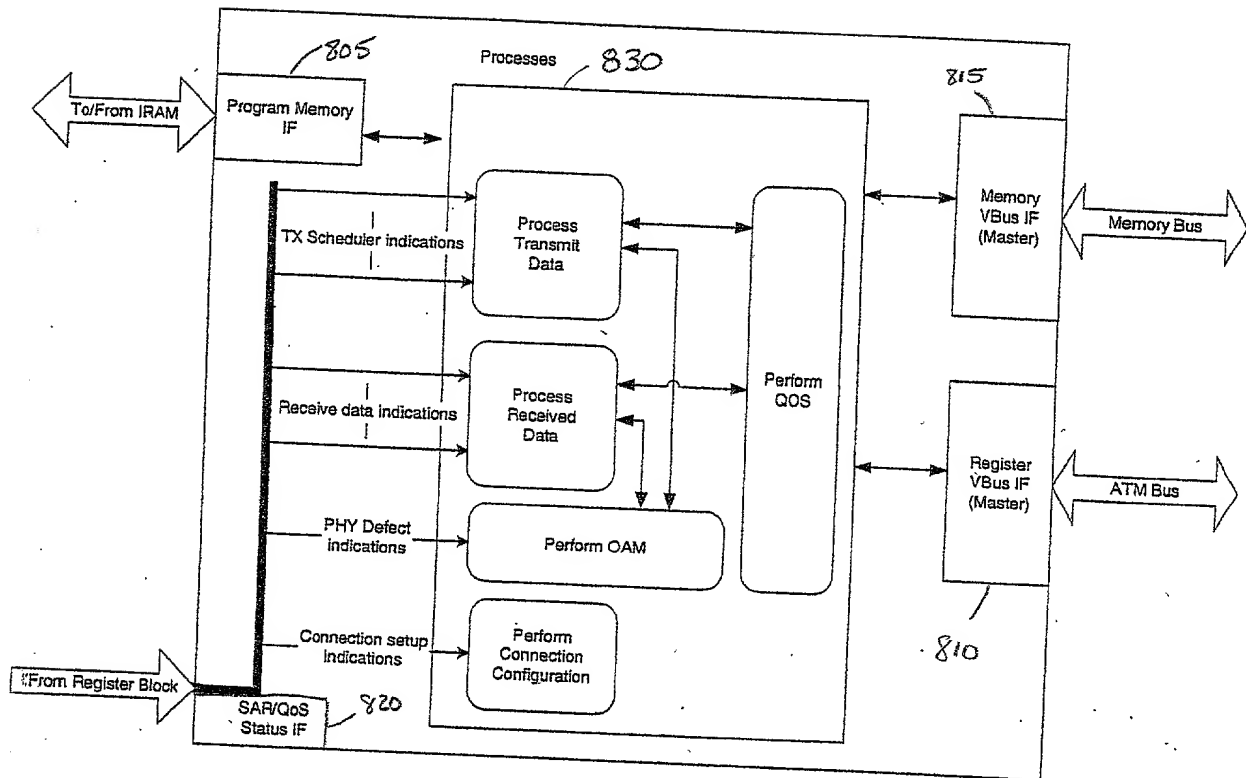


Figure 8